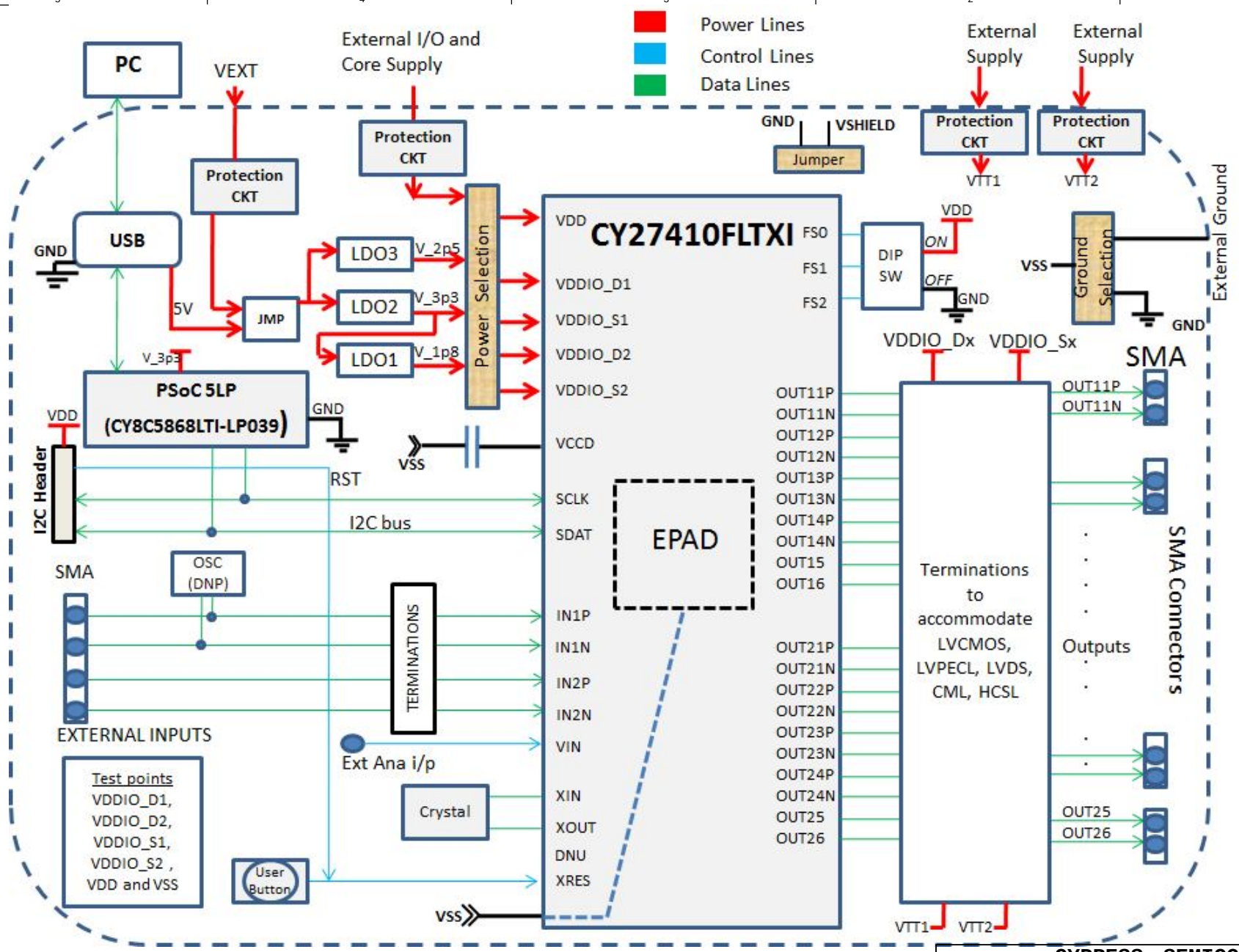
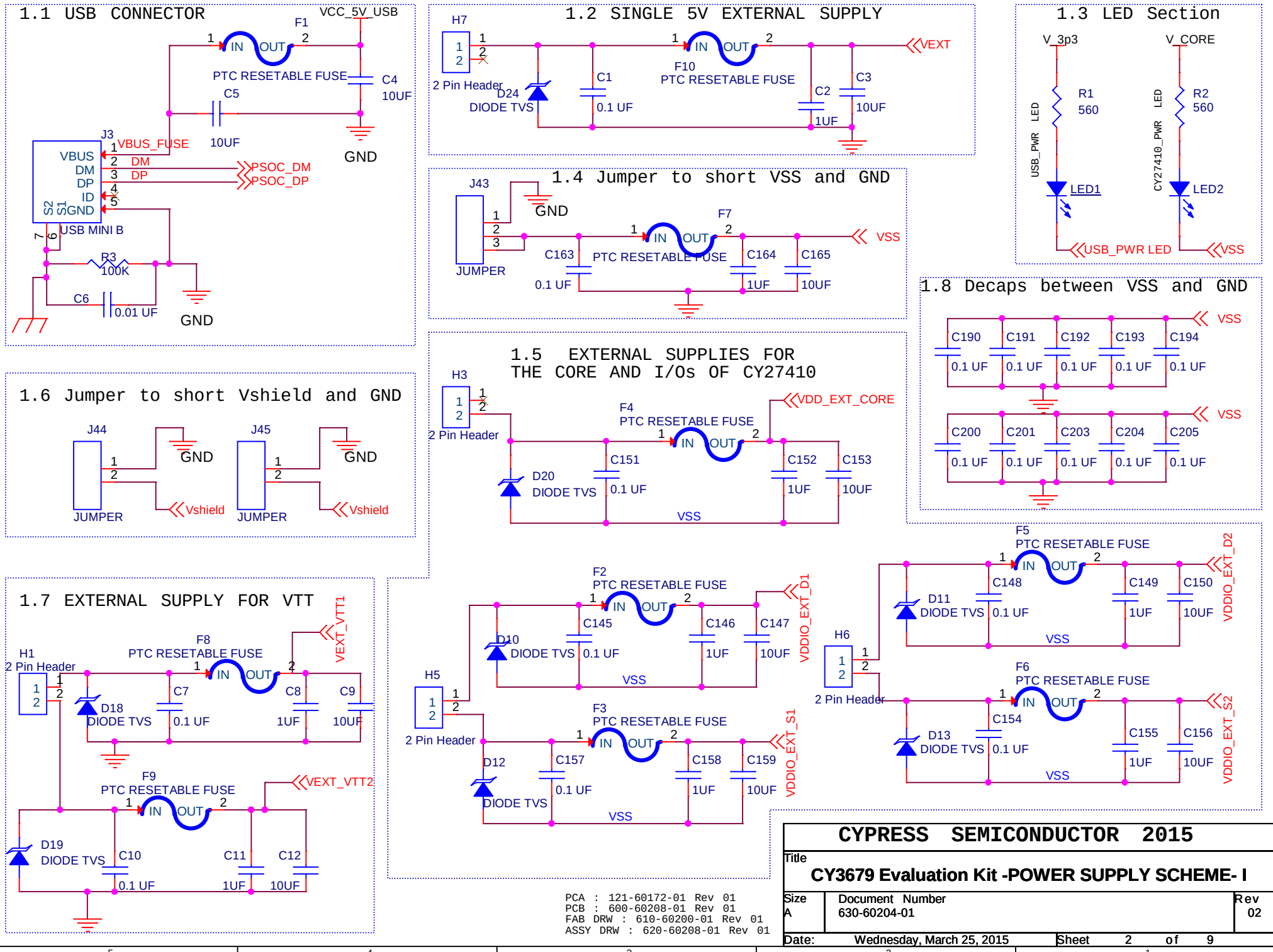


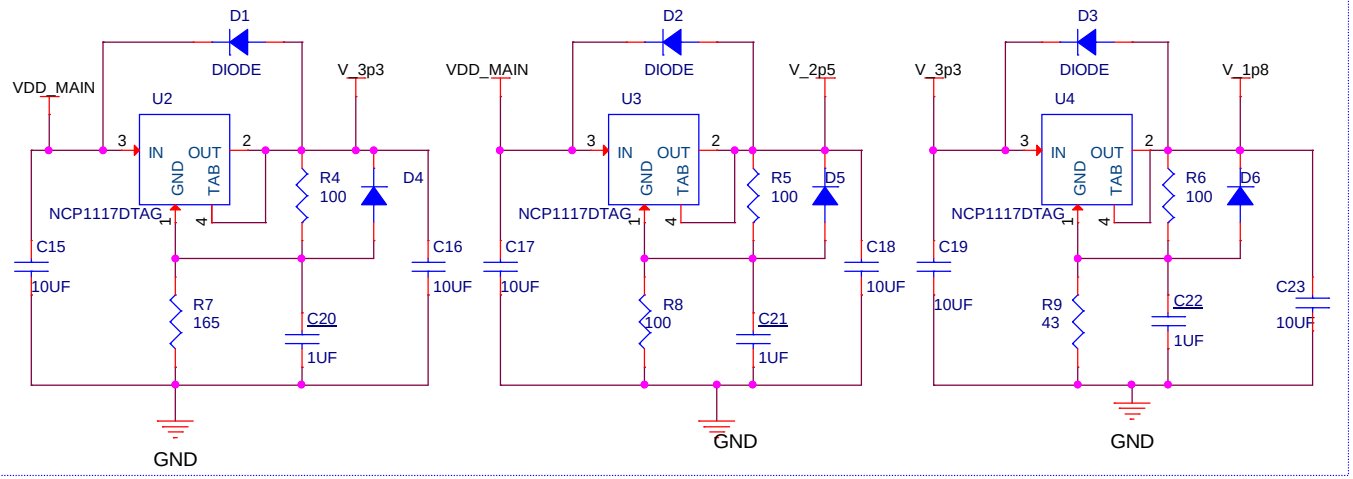

# CY3679 EVALUATION KIT - BLOCK DIAGRAM

PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

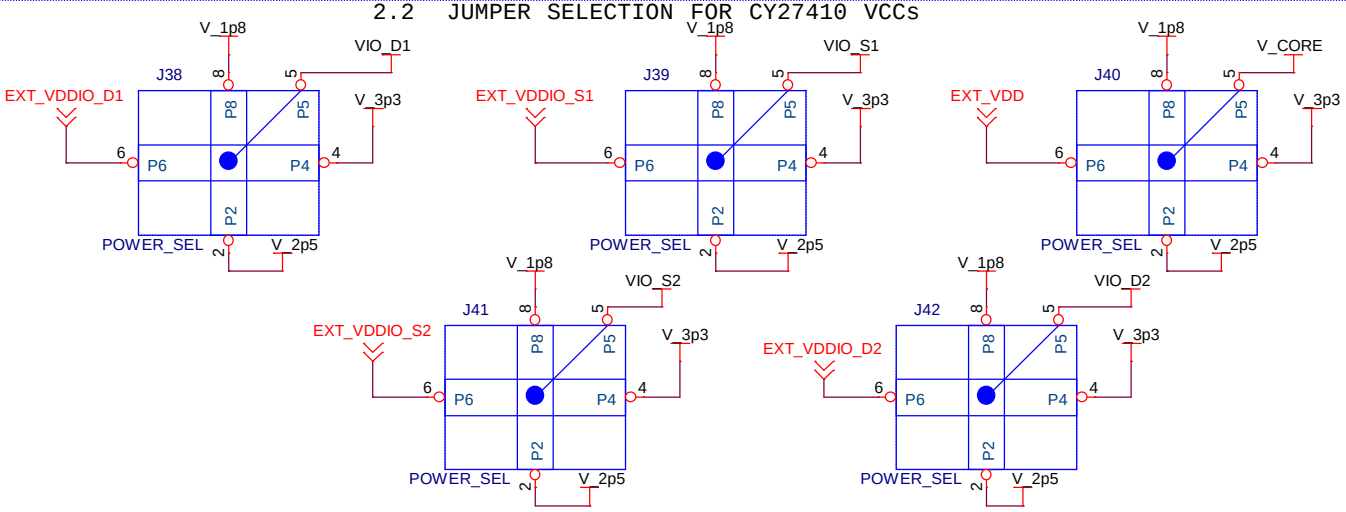
| CYPRESS SEMICONDUCTOR 2015 | | |
|---|---|---|
| Title: CY3679 Evaluation Kit - BLOCK DIAGRAM | | |
| Size: A4 | Document Number: 630-60204-01 | Rev: 02 |
| Date: Wednesday, March 25, 2015 | Sheet 1 of 9 | |


1.1 USB CONNECTOR
VCC_5V_USB
F1
PTC RESETABLE FUSE
C4
10UF
C5
10UF
GND
J3
VBUS_FUSE
DM
DP
ID
GND
USB MINI B
PSOC_DM
PSOC_DP
R3
100K
C6
0.01 UF
GND
1.2 SINGLE 5V EXTERNAL SUPPLY
H7
2 Pin Header
D24
DIODE TVS
C1
0.1 UF
F10
PTC RESETABLE FUSE
C2
1UF
C3
10UF
VEXT
1.3 LED Section
V_3p3
V_CORE
R1
560
R2
560
USB_PWR LED
CY27410_PWR LED
LED1
LED2
USB_PWR LED
VSS
1.4 Jumper to short VSS and GND
J43
JUMPER
GND
F7
PTC RESETABLE FUSE
C163
0.1 UF
C164
1UF
C165
10UF
VSS
1.8 Decaps between VSS and GND
C190
C191
C192
C193
C194
C200
C201
C203
C204
C205
0.1 UF
VSS
1.5 EXTERNAL SUPPLIES FOR THE CORE AND I/Os OF CY27410
H3
2 Pin Header
F4
PTC RESETABLE FUSE
VDD_EXT_CORE
D20
DIODE TVS
C151
0.1 UF
C152
1UF
C153
10UF
VSS
1.6 Jumper to short Vshield and GND
J44
J45
JUMPER
GND
Vshield
1.7 EXTERNAL SUPPLY FOR VTT
H1
2 Pin Header
F8
PTC RESETABLE FUSE
VEXT_VTT1
D18
DIODE TVS
C7
0.1 UF
C8
1UF
C9
10UF
F9
PTC RESETABLE FUSE
VEXT_VTT2
D19
DIODE TVS
C10
0.1 UF
C11
1UF
C12
10UF
F2
PTC RESETABLE FUSE
VDDIO_EXT_D1
H5
2 Pin Header
D10
DIODE TVS
C145
0.1 UF
C146
1UF
C147
10UF
VSS
F3
PTC RESETABLE FUSE
VDDIO_EXT_S1
D12
DIODE TVS
C157
0.1 UF
C158
1UF
C159
10UF
VSS
F5
PTC RESETABLE FUSE
VDDIO_EXT_D2
H6
2 Pin Header
D11
DIODE TVS
C148
0.1 UF
C149
1UF
C150
10UF
VSS
F6
PTC RESETABLE FUSE
D13
DIODE TVS
C154
0.1 UF
C155
1UF
C156
10UF
VSS
PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01
CYPRESS SEMICONDUCTOR 2015
Title
CY3679 Evaluation Kit -POWER SUPPLY SCHEME- I
Size
A
Document Number
630-60204-01
Rev
02
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2
of
9

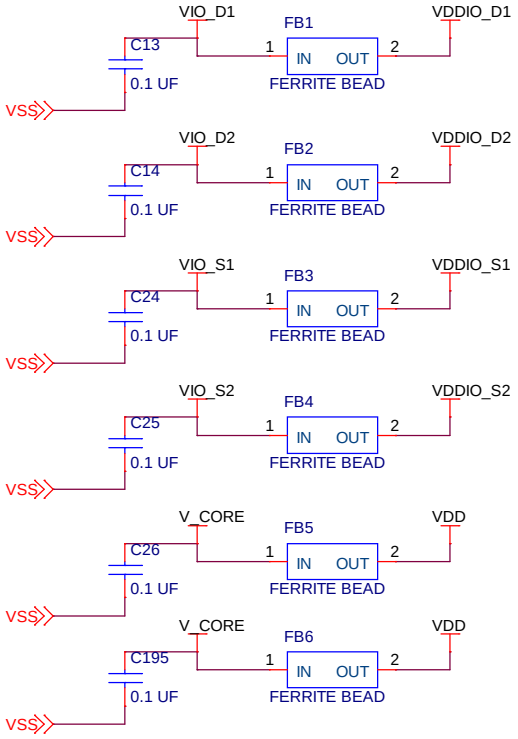
## 2.1 VOLTAGE REGULATOR



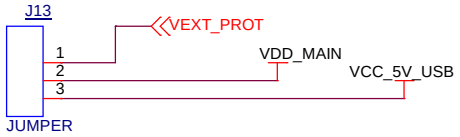

## 2.2 JUMPER SELECTION FOR CY27410 VCCs



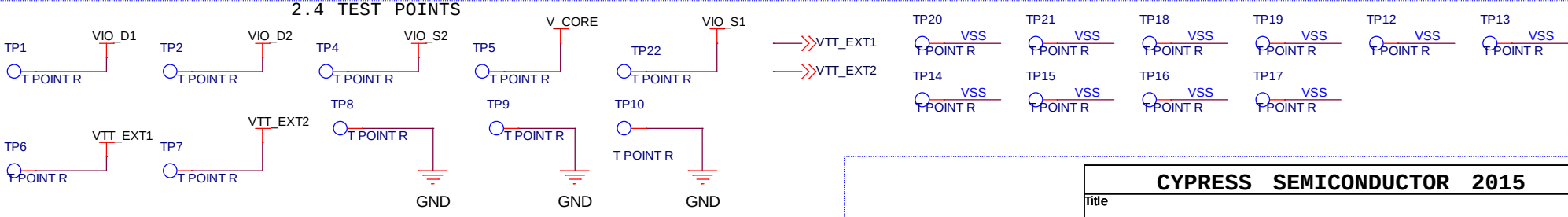

## 2.3 FERRITE BEADS FOR CY27410 SUPPLIES




## 2.5 LDO Input Power Selection




## 2.4 TEST POINTS




Note - All the test points (TP) listed in Section 2.4 and Test Point TP11 are NOT loaded by default i.e. they are all DNM

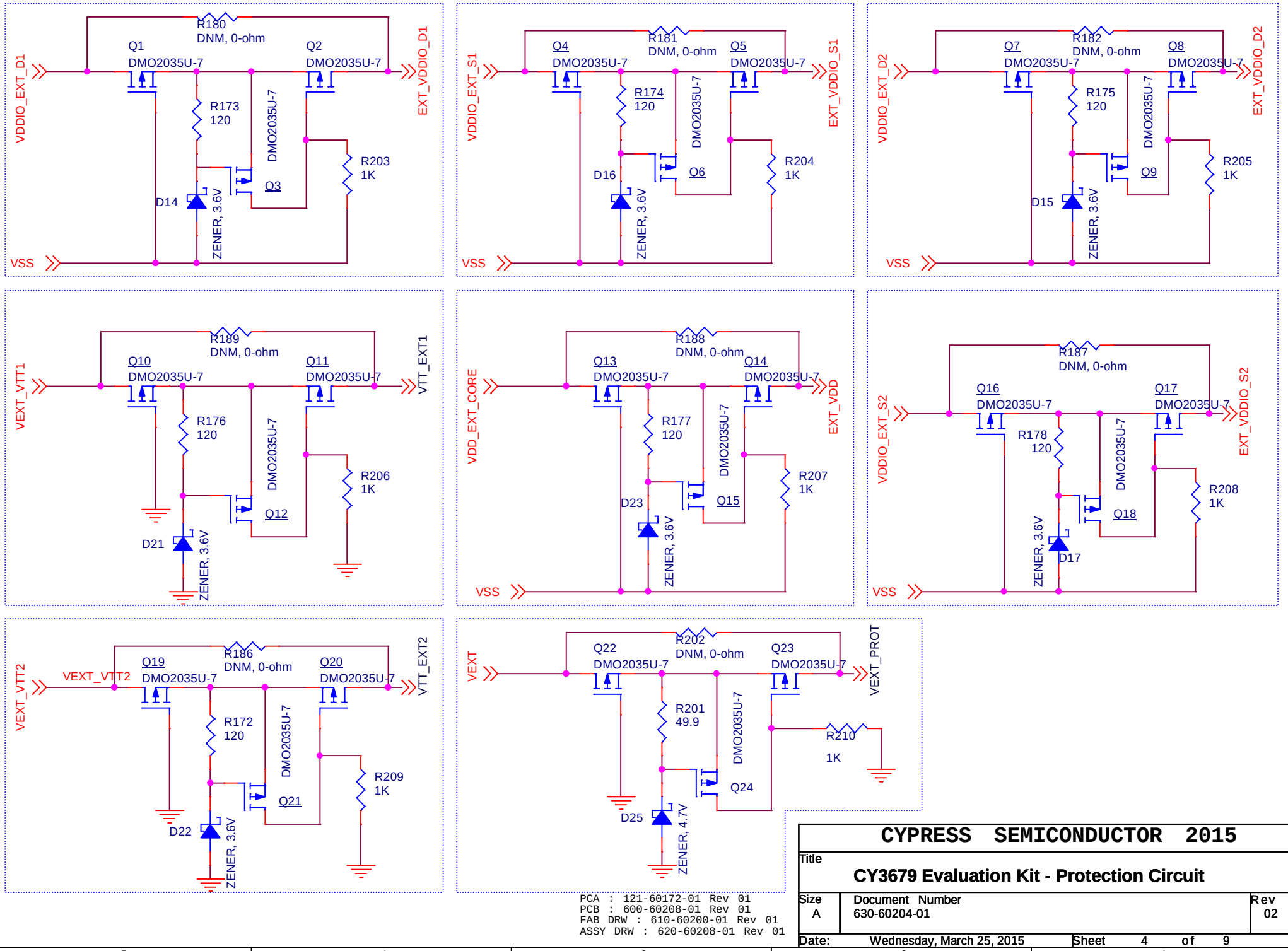
PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

**CYPRESS SEMICONDUCTOR 2015**

Title
**CY3679 Evaluation Kit -POWER SUPPLY SCHEME-II**

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PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

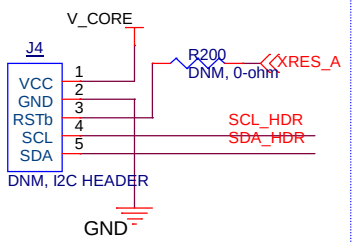
**CYPRESS SEMICONDUCTOR 2015**

Title: **CY3679 Evaluation Kit - Protection Circuit**

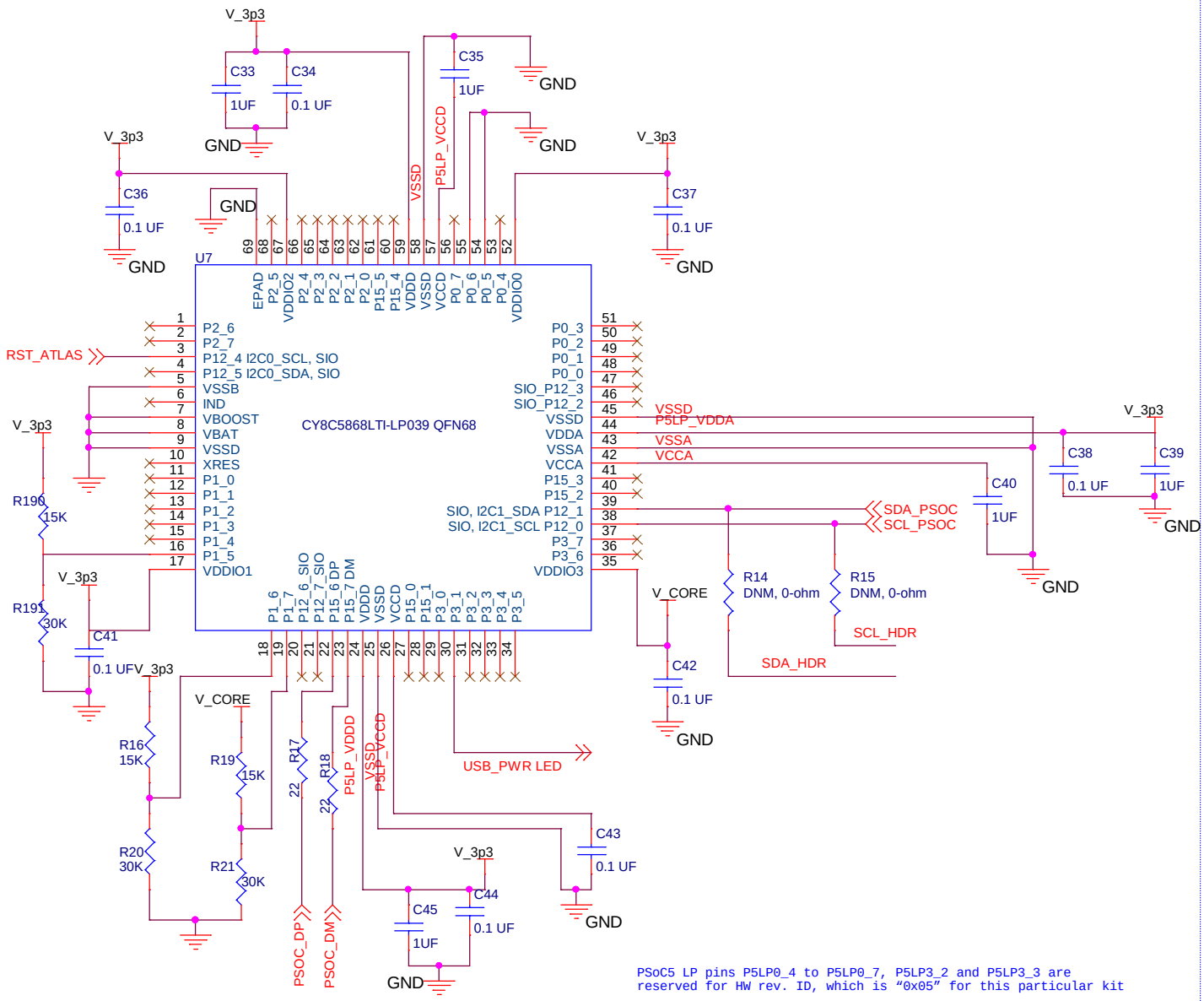
| Size | Document Number | Rev |
|---|---|---|
| A | 630-60204-01 | 02 |

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## 3.1 5-PIN I2C HEADER



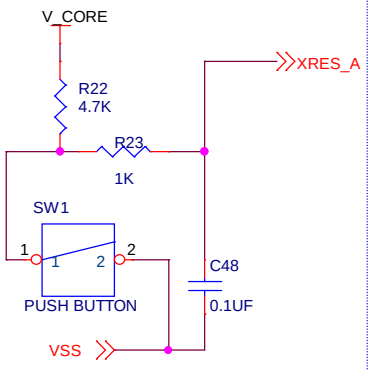

## 3.2 PSoC_5LP Connections



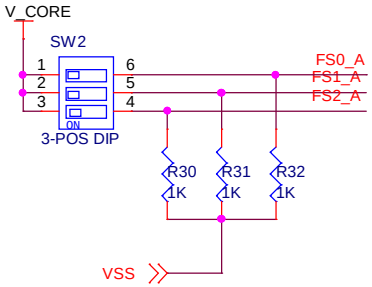

PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

| | | |
|---|---|---|
| CYPRESS SEMICONDUCTOR 2015 | | |
| Title: CY3679 Evaluation Kit -CONTROLLER_PSoC5LP | | |
| Size: A4 | Document Number: 630-60204-01 | Rev: 02 |
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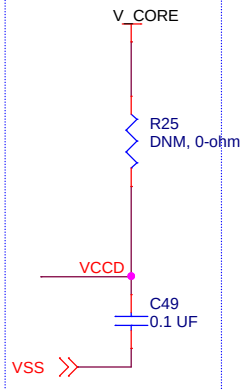
## 4.1 RESET CIRCUIT



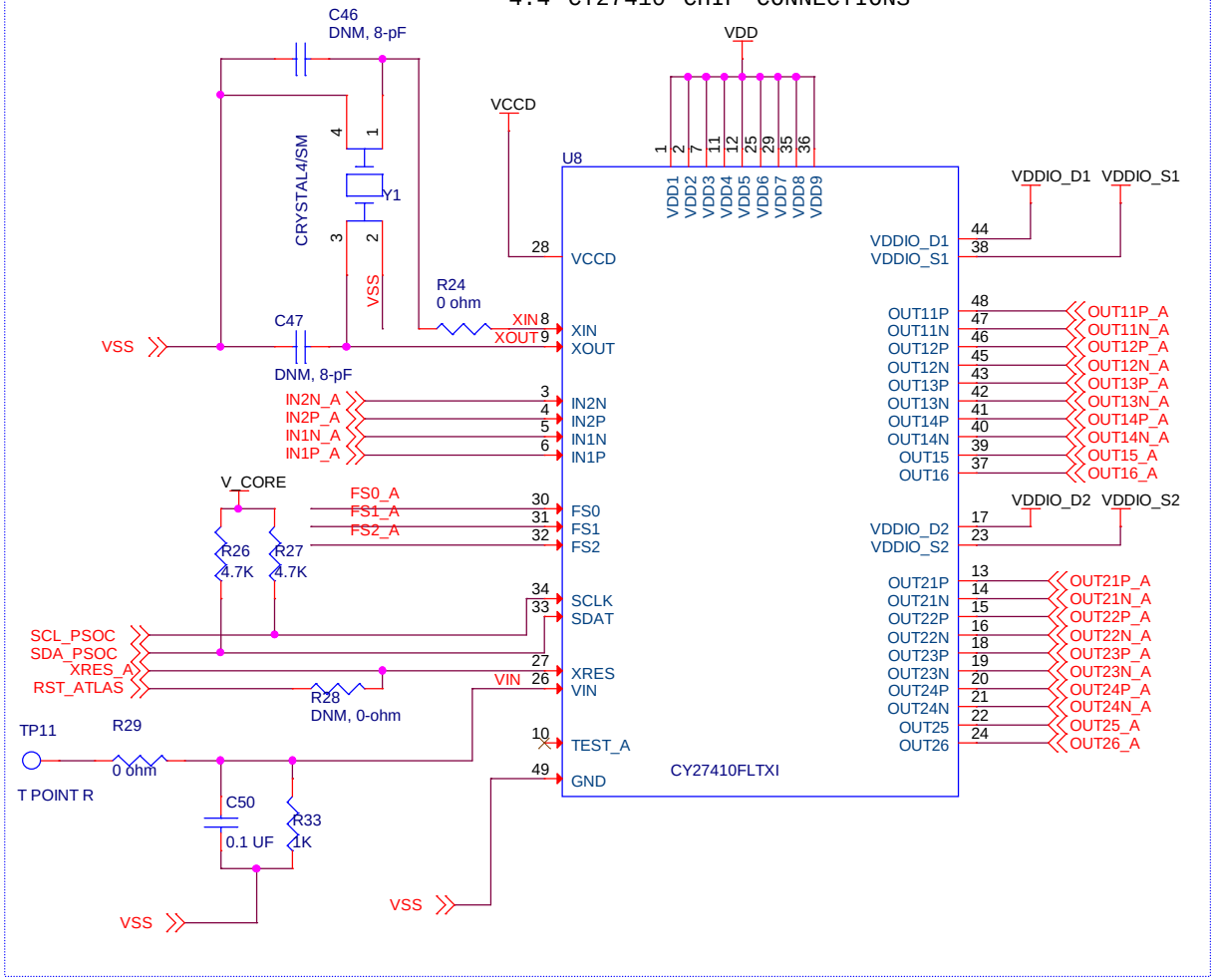

## 4.2 FS[2:0] PIN SELECTION JUMPERS

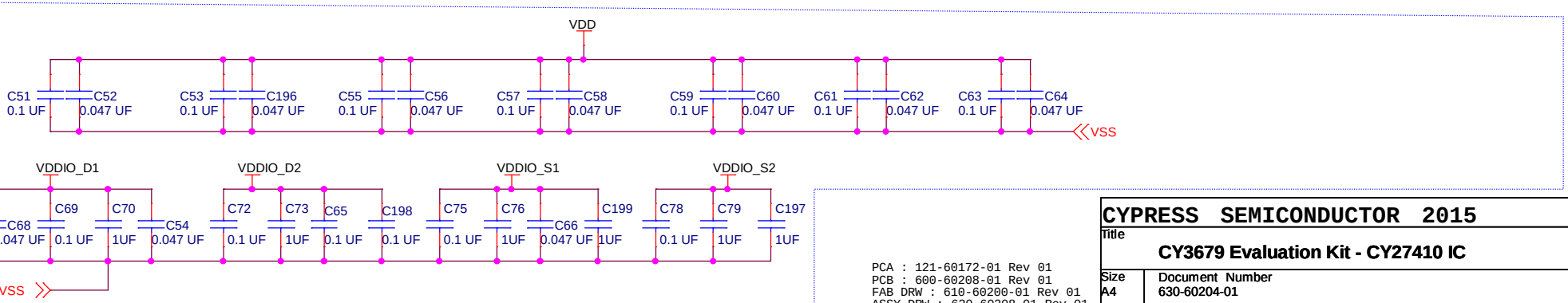



## 4.3 VCCD AS PER DATASHEET




## 4.4 CY27410 CHIP CONNECTIONS




PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

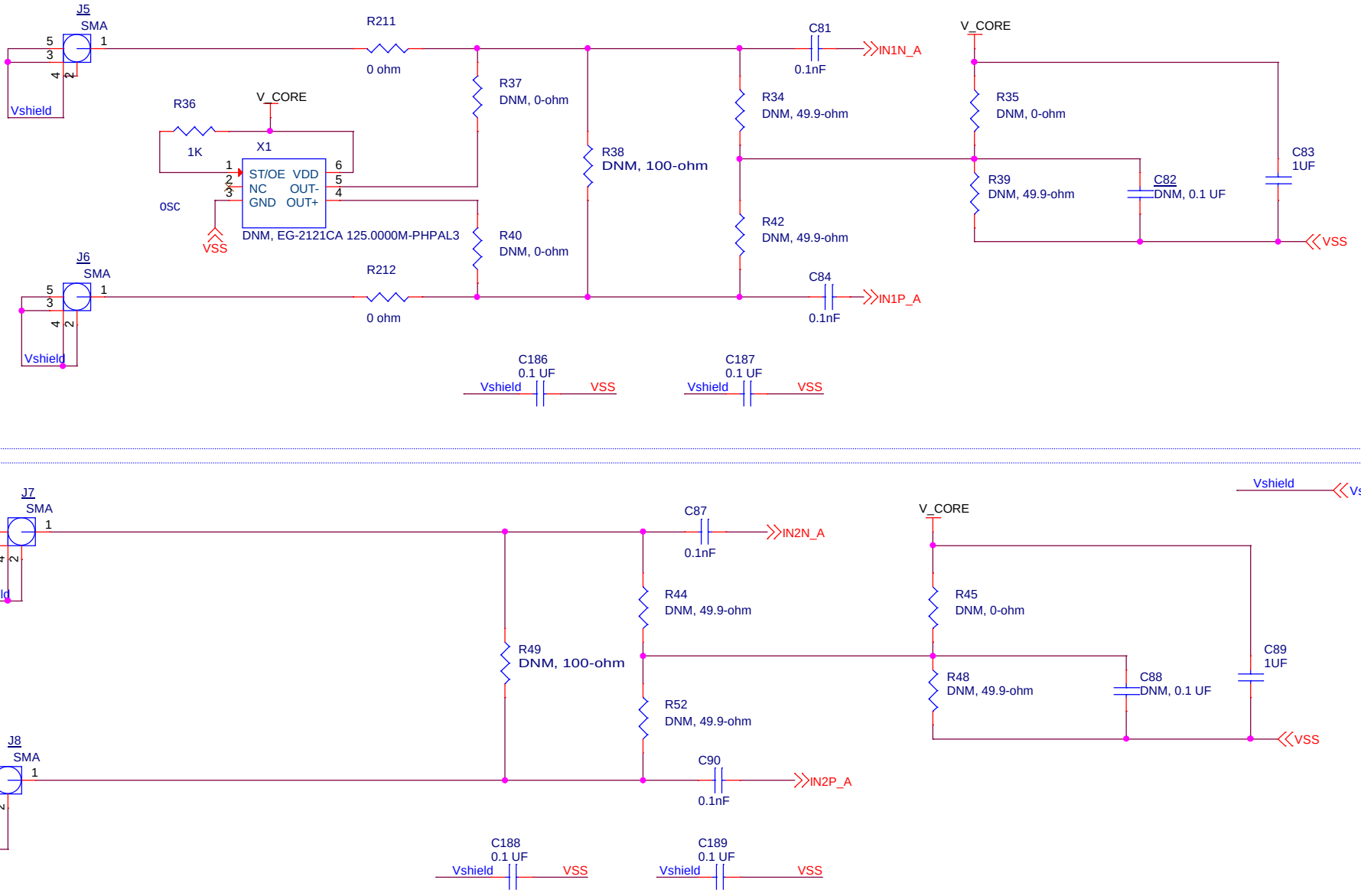
**CYPRESS SEMICONDUCTOR 2015**

Title: **CY3679 Evaluation Kit - CY27410 IC**

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|---|---|---|
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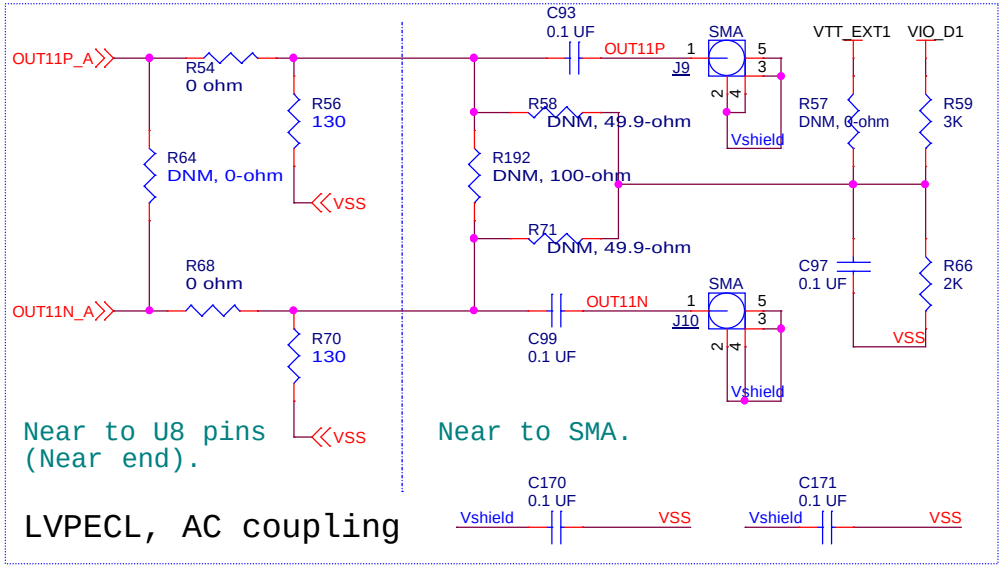
# 4.1 INPUT TERMINATIONS

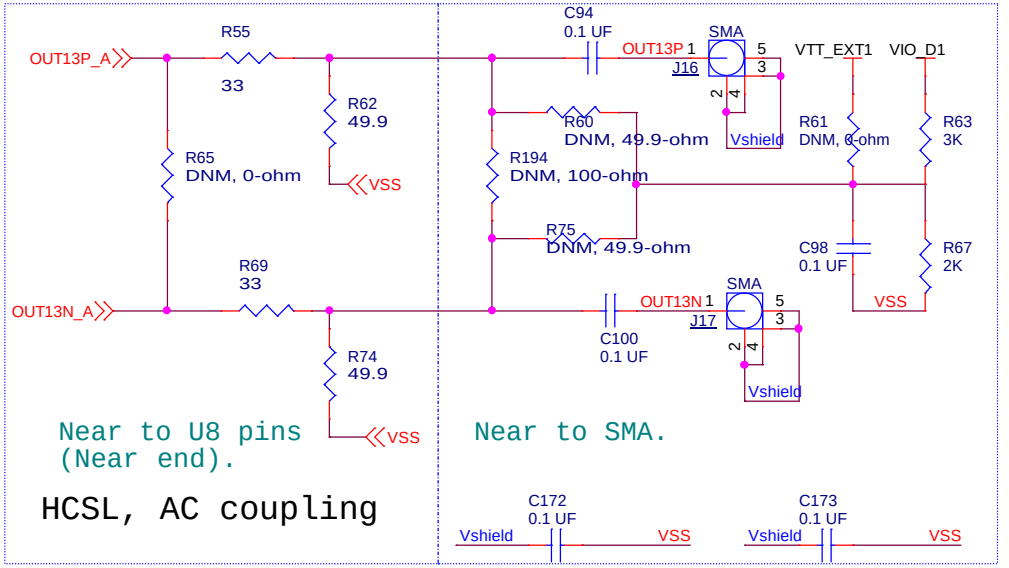


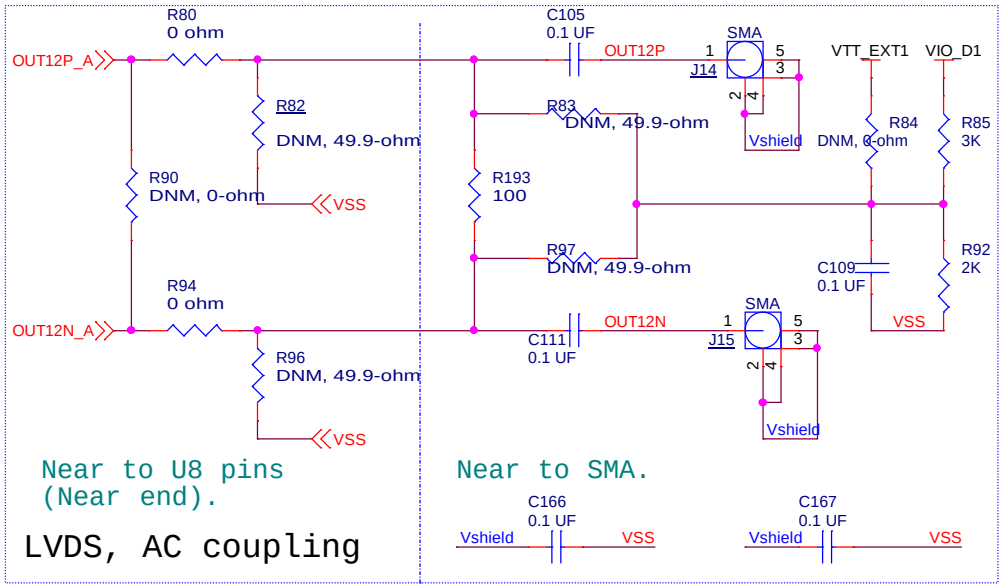

PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

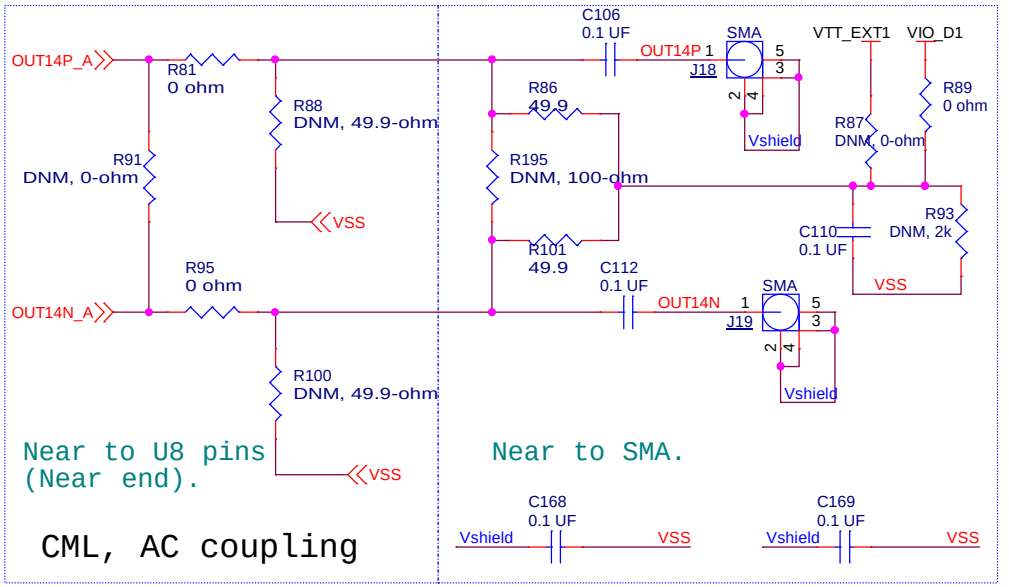
| | | |
|---|---|---|
| CYPRESS SEMICONDUCTOR 2015 | | |
| Title: CY3679 Evaluation Kit -Input Termination | | |
| Size: A4 | Document Number: 630-60204-01 | Rev: 02 |
| Date: Wednesday, March 25, 2015 | Sheet 7 of 9 | |

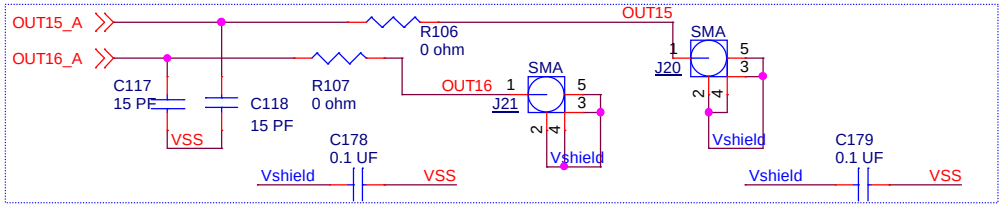
## 5. Output Terminations for BANK 1




VTT_EXT1
Vshield

PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

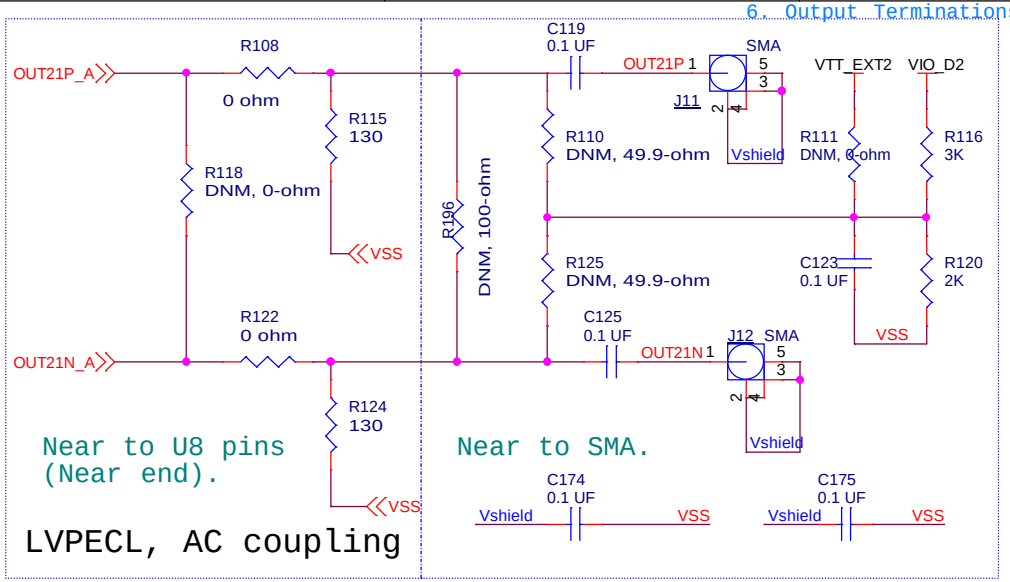
**CYPRESS SEMICONDUCTOR 2015**

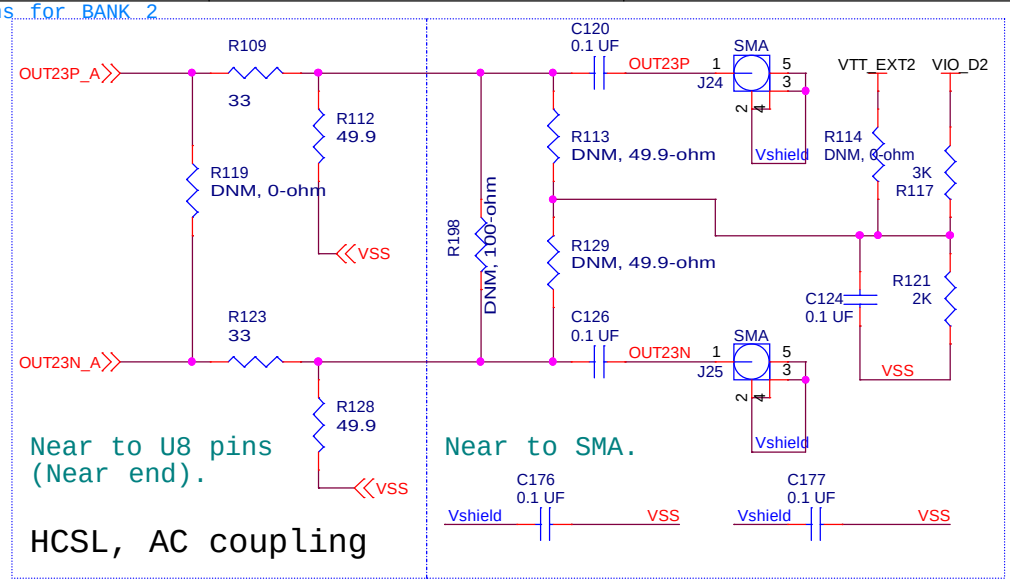
Title: **CY3679 Evaluation Kit -O/P TERMINATIONS - BANK 1**

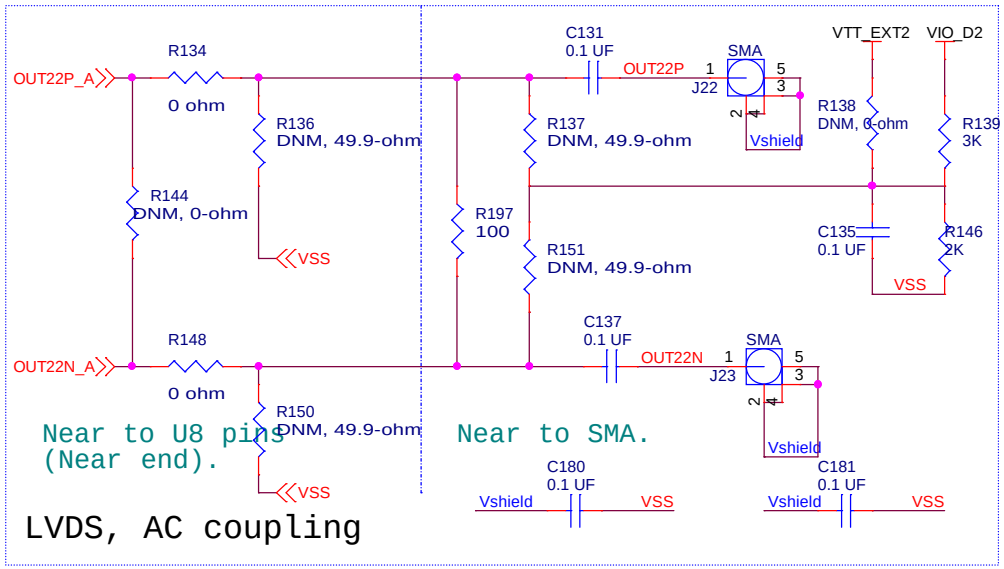
| Size | Document Number | Rev |
|---|---|---|
| A4 | 630-60204-01 | 02 |

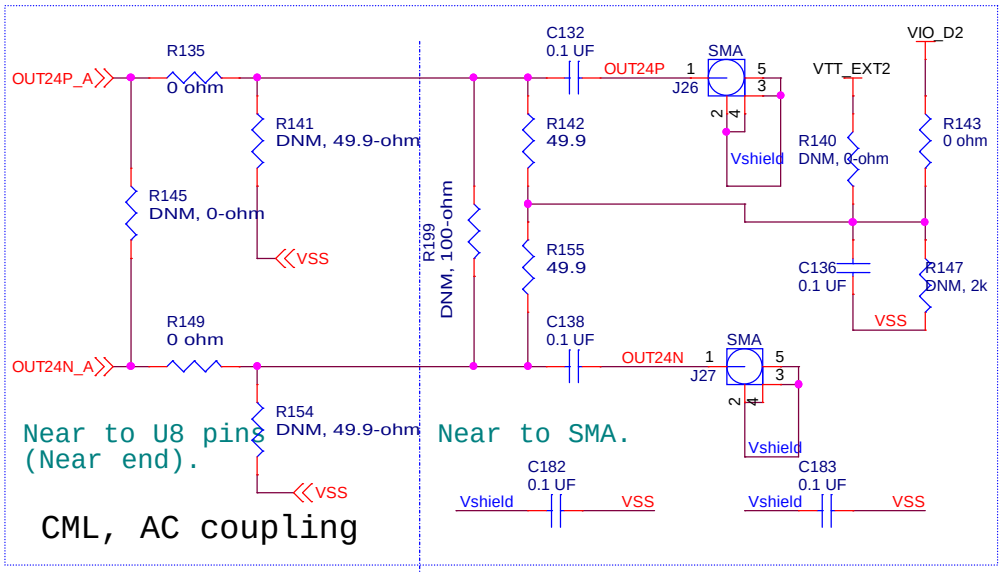
Date: Wednesday, March 25, 2015 Sheet 8 of 9

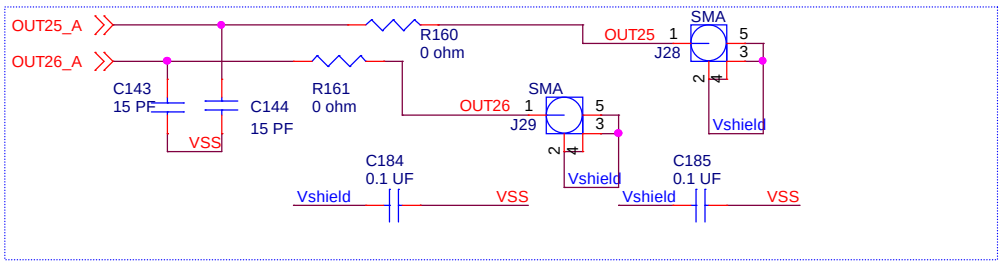
# 6. Output Terminations for BANK 2




VTT_EXT2

Vshield Vshield

PCA : 121-60172-01 Rev 01
PCB : 600-60208-01 Rev 01
FAB DRW : 610-60200-01 Rev 01
ASSY DRW : 620-60208-01 Rev 01

**CYPRESS SEMICONDUCTOR 2015**

Title: **CY3679 Evaluation Kit -O/P TERMINATIONS - BANK 2**

| Size | Document Number | Rev |
|---|---|---|
| A4 | 630-60204-01 | 02 |

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